TOSHIBA Photocoupler GaAs Ired & Photo−Transistor

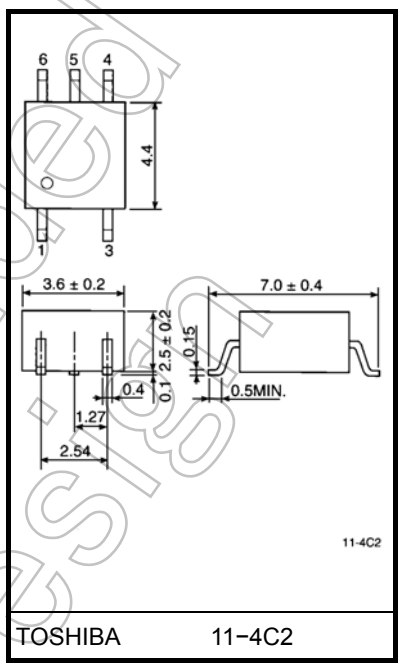
TLP130

Programmable Controllers

AC / DC−Input Module

Telecommunication

Unit in mm



TOSHIBA 11−4C2

Weight: 0.09 g (typ.)

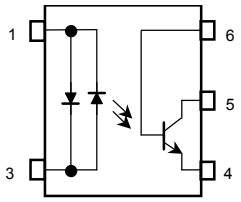
The TOSHIBA mini flat coupler TLP130 is a small outline coupler, suitable for surface mount assembly.
TLP130 consists of a photo transistor, optically coupled to two gallium arsenide infrared emitting diode connected inverse parallel, and operate directly by AC input current.

- Collector−emitter voltage: 80V(min.)
- Current transfer ratio: 50%(min.)
  Rank GB: 100%(min.)
- Isolation voltage: 3750Vrms(min.)
- UL recognized: UL1577, file no.E67349
- Current transfer ratio

Classi−fication	Current Transfer Ratio I_F = 5mA, V_{CE} = 5V, Ta = 25°C		Marking Of Classification
	Min.	Max.	
Standard	50	600	Blank, Y, GR, GB
Rank GB	100	600	GB,GR

(Note) Application type name for certification test, please use standard product type name, i.e. TLP130(GB): TLP130

Pin Configurations (top view)



1 : Anode, Cathode
3 : Cathode, Anode
4 : Emitter
5 : Collector
6 : Base

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit
LED	Forward current	$I_{F(RMS)}$	50	mA
	Forward current derating (Ta≥53°C)	ΔI_F / °C	−0.7	mA / °C
	Peak forward current (100μs pulse,100pps)	I_{FP}	1	A
	Junction temperature	T_j	125	°C
Detector	Collector−emitter voltage	V_{CEO}	80	V
	Collector−base voltage	V_{CBO}	80	V
	Emitter−collector voltage	V_{ECO}	7	V
	Emitter−base voltage	V_{EBO}	7	V
	Collector current	I_C	50	mA
	Peak collector current (10ms pulse,100pps)	I_{CP}	100	mA
	Power dissipation	P_C	150	mW
	Power dissipation derating (Ta≥25°C)	ΔP_C / °C	−1.5	mW / °C
	Junction temperature	T_j	125	°C
Storage temperature range		T_{stg}	−55~125	°C
Operating temperature range		T_{opr}	−55~100	°C
Lead soldering temperature (10s)		T_{sol}	260	°C
Total package power dissipation		P_T	200	mW
Total package power dissipation derating (Ta≥25°C)		ΔP_T / °C	−2.0	mW / °C
Isolation voltage (AC, 1min., RH ≤ 60%) (Note 1)		BV_S	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1) Device considered a two terminal device: Pins 1 and 3 shorted together and pins 4, 5 and 6 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Supply voltage	V_{CC}	—	5	48	V
Forward current	$I_{F(RMS)}$	—	16	25	mA
Collector current	I_C	—	1	10	mA
Operating temperature	T_{opr}	−25	—	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

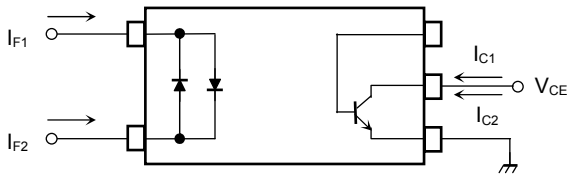
Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
LED	Forward voltage	V_F	I_F = ±10mA	1.0	1.15	1.3	V
	Capacitance	C_T	V = 0, f = 1MHz	—	60	—	pF
Detector	Collector−emitter breakdown voltage	$V_{(BR)CEO}$	I_C = 0.5mA	80	—	—	V
	Emitter−collector breakdown voltage	$V_{(BR)ECO}$	I_E = 0.1mA	7	—	—	V
	Collector−base breakdown voltage	$V_{(BR)CBO}$	I_C = 0.1mA	80	—	—	V
	Emitter−base breakdown voltage	$V_{(BR)EBO}$	I_E = 0.1mA	7	—	—	V
	Collector dark current	I_{CEO}	V_{CE} = 48V	—	10	100	nA
			V_{CE} = 48V, Ta = 85°C	—	2	50	μA
	Collector dark current	I_{CER}	V_{CE} = 48V, Ta = 85°C R_{BE} = 1MΩ	—	0.5	10	μA
	Collector dark current	I_{CBO}	V_{CB} = 10V	—	0.1	—	nA
	DC forward current gain	h_{FE}	V_{CE} = 5V, I_C = 0.5mA	—	400	—	—
	Capacitance collector to emitter	C_{CE}	V = 0 , f = 1MHz	—	10	—	pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Current transfer ratio	I_C / I_F	I_F = ±5mA, V_{CE} = 5V	50	—	600	%
		Rank GB	100	—	600	
Saturated CTR	I_C / $I_{F(sat)}$	I_F = ±1mA, V_{CE} = 0.4V	—	60	—	%
		Rank GB	30	—	—	
Base photo−current	I_{PB}	I_F = ±5mA, V_{CB} = 5V	—	10	—	μA
Collector-emitter saturation voltage	$V_{CE(sat)}$	I_C = 2.4mA, I_F = ±8mA	—	—	0.4	V
		I_C = 0.2mA, I_F = ±1mA	—	0.2	—	
		Rank GB	—	—	0.4	
Off−state collector current	$I_{C(off)}$	I_F = ±0.7mA, V_{CE} = 48V	—	1	10	μA
CTR symmetry	$I_{C(ratio)}$	I_C(I_F = −5mA) / I_C(I_F = 5mA) (Note 2)	0.33	—	3	—

(Note 2) $I_{C(ratio)} = \dfrac{I_{C2}(I_F = I_{F2},\ V_{CE} = 5V)}{I_{C1}(I_F = I_{F1},\ V_{CE} = 5V)}$



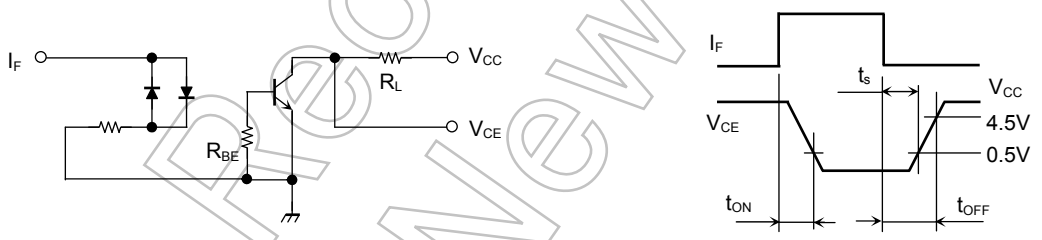
Isolation Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Capacitance input to output	C_S	V_S=0, f=1MHz	—	0.8	—	pF
Isolation resistance	R_S	V_S=500V	5×10^{10}	10^{14}	—	Ω
Isolation voltage	BV_S	AC, 1minute	3750	—	—	Vrms
		AC, 1second, in oil	—	10000	—	
		DC, 1 minute, in oil	—	10000	—	Vdc

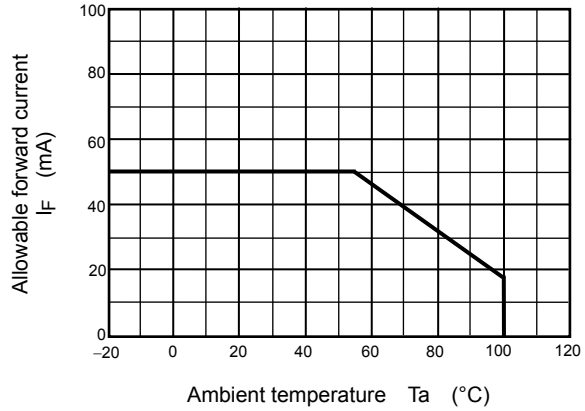
Switching Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Rise time	t_r	V_{CC} = 10V, I_C = 2mA R_L = 100Ω	—	2	—	μs
Fall time	t_f		—	3	—	
Turn−on time	t_{on}		—	3	—	
Turn−off time	t_{off}		—	3	—	
Turn−on time	t_{ON}	R_L = 1.9 kΩ (Fig.1) R_{BE} = OPEN V_{CC} = 5 V, I_F = ±16mA	—	2	—	μs
Storage time	t_S		—	25	—	
Turn-off time	t_{OFF}		—	40	—	
Turn−on time	t_{ON}	R_L = 1.9kΩ (Fig.1) R_{BE} = 220kΩ V_{CC} = 5 V, I_F = ±16mA	—	2	—	μs
Storage time	t_S		—	20	—	
Turn-off time	t_{OFF}		—	30	—	

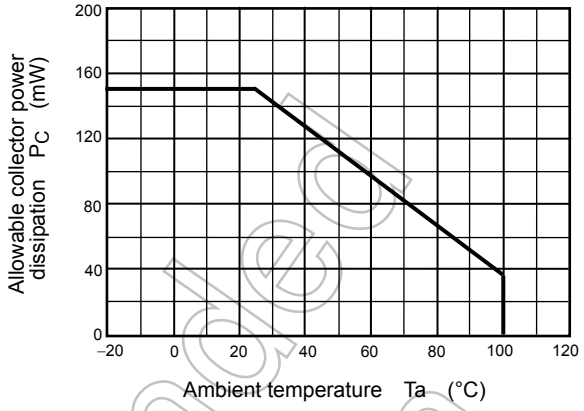
Fig. 1 Switching time test circuit



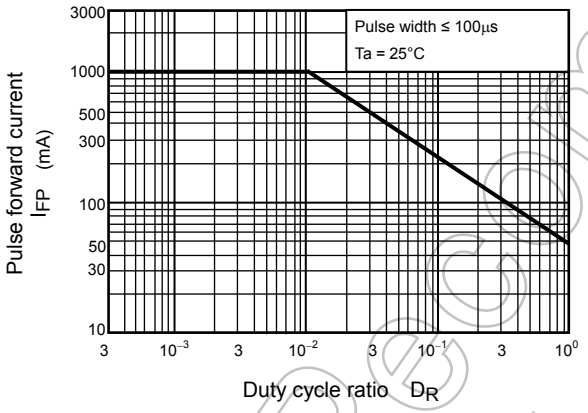
I_F – Ta



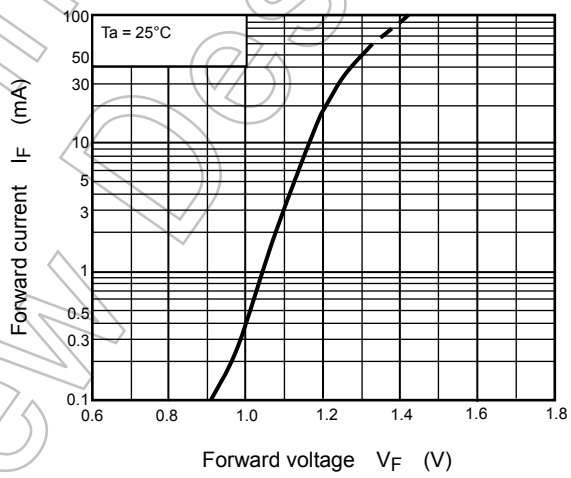
P_C – Ta



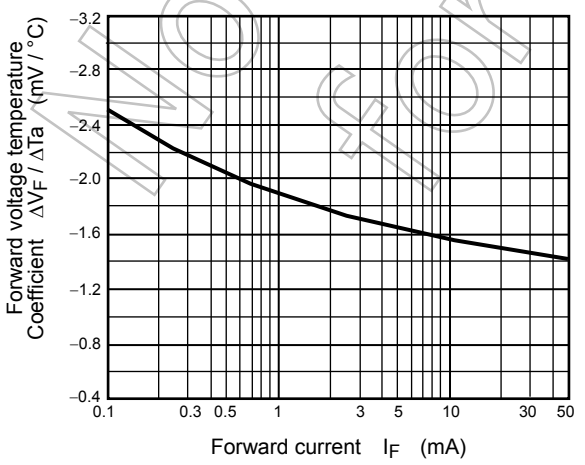
I_{FP} – D_R



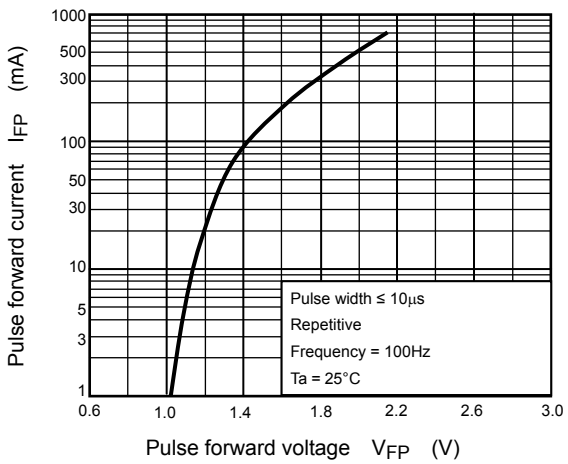
I_F – V_F



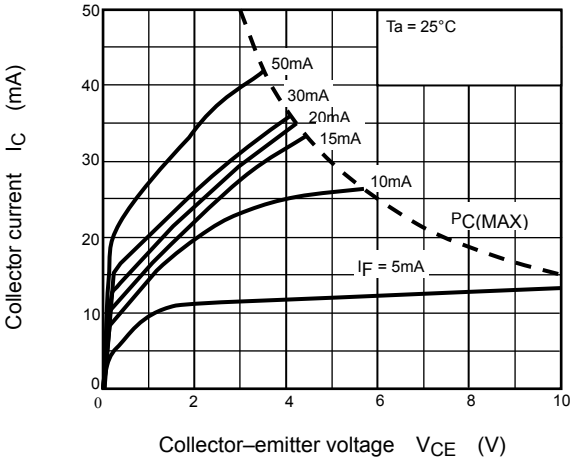
$\Delta V_F / \Delta Ta$ – I_F



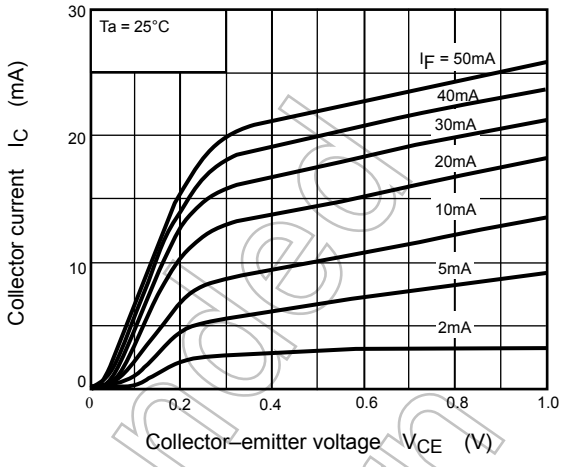
I_{FP} – V_{FP}



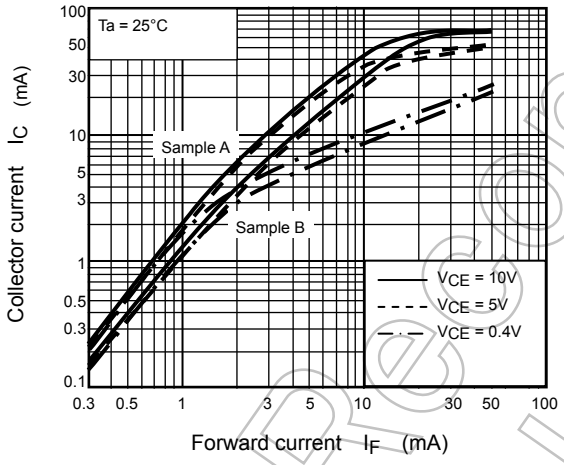
$I_C - V_{CE}$



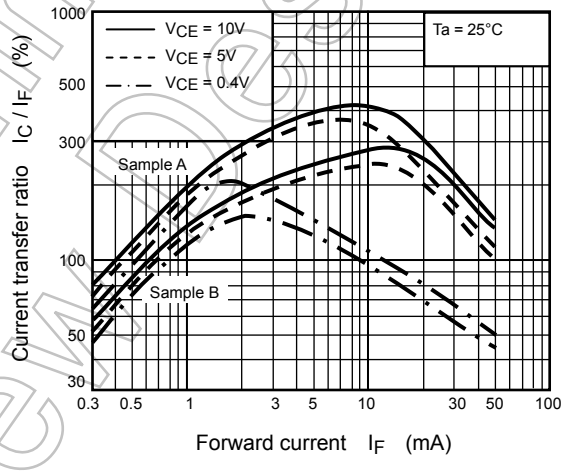
$I_C - V_{CE}$



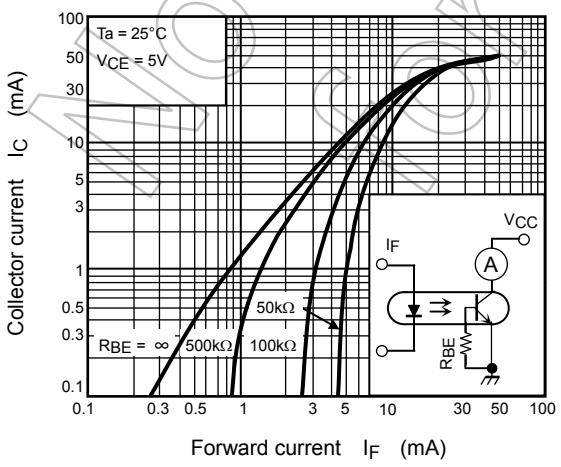
$I_C - I_F$



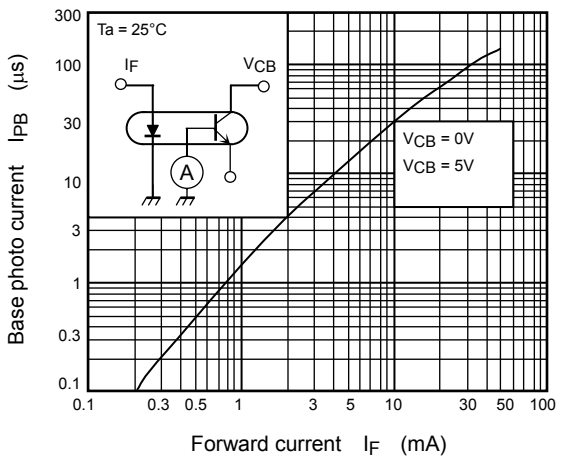
$I_C / I_F - I_F$



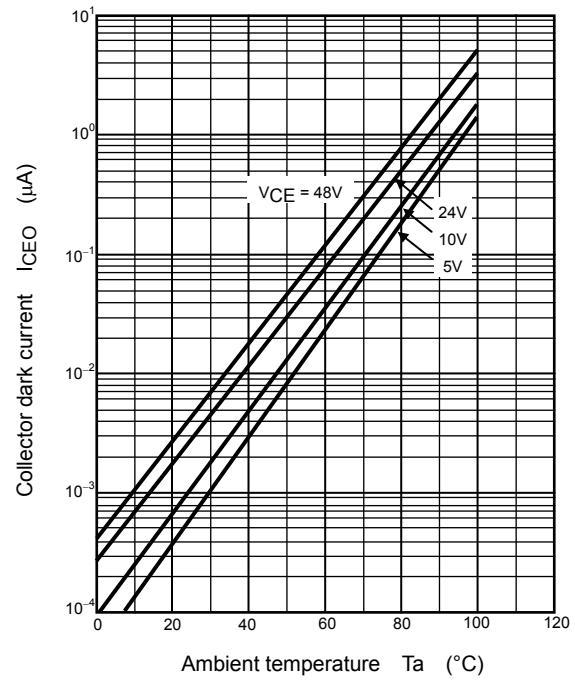
$I_C - I_F$ at R_{BE}



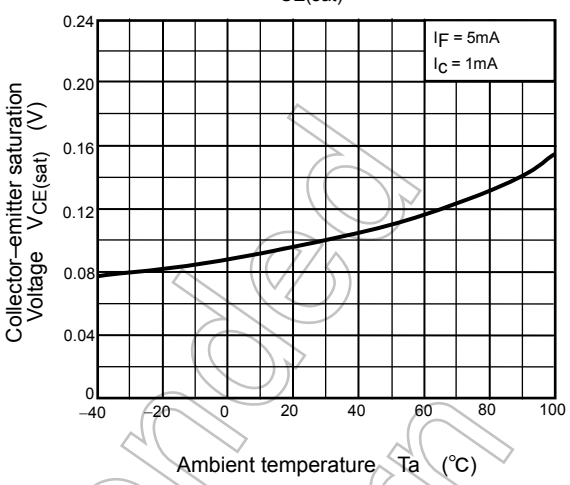
$I_{PB} - I_F$



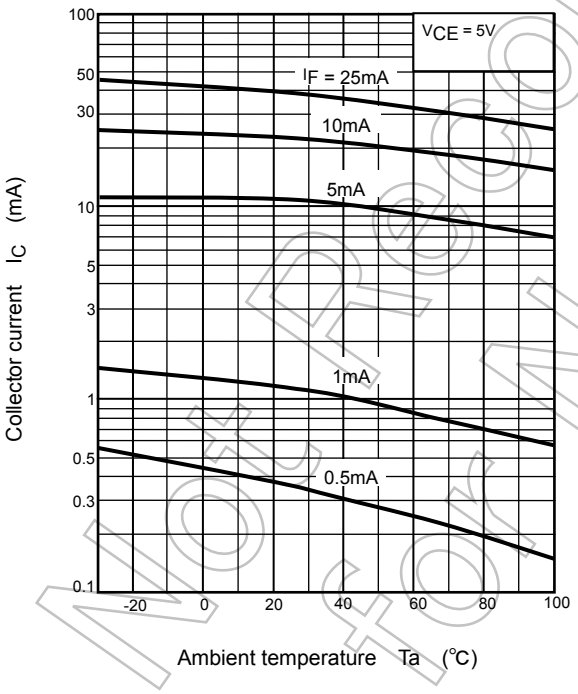
I_{CEO} – Ta



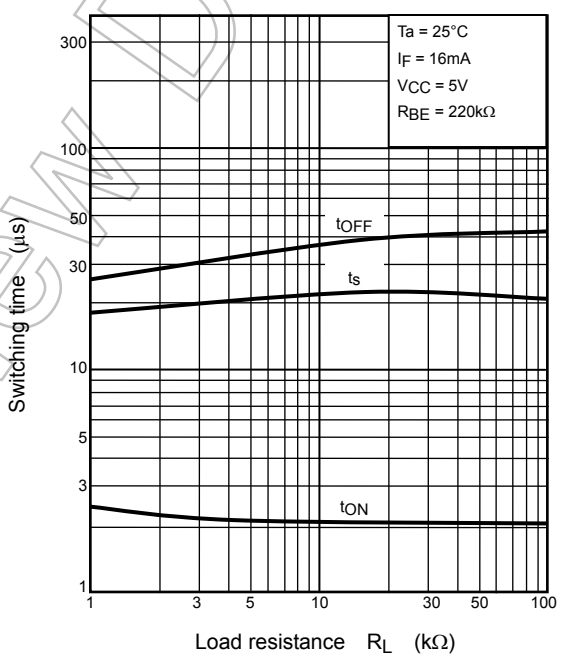
$V_{CE(sat)}$ – Ta



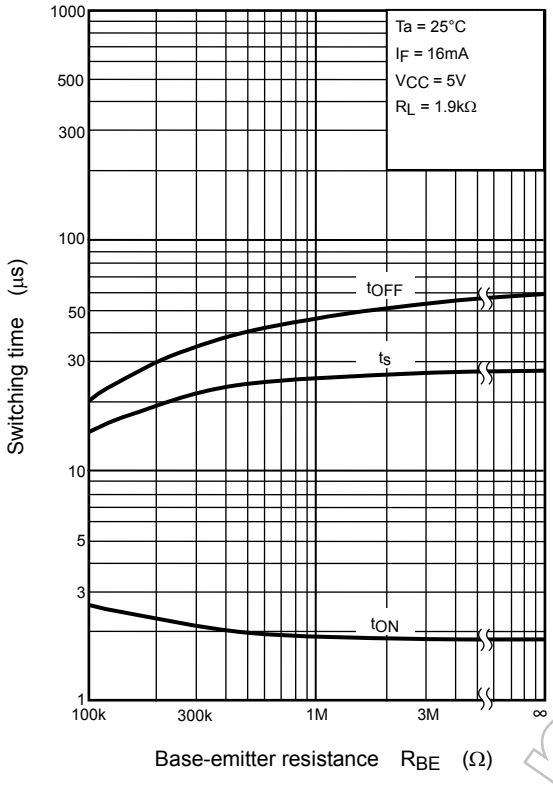
I_C – Ta



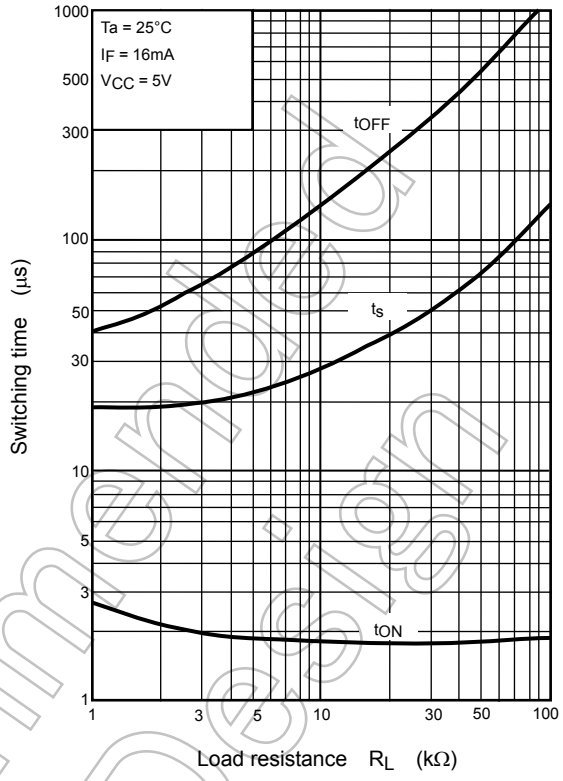
Switching Time – R_L



Switching Time – R_{BE}



Switching Time – R_L



RESTRICTIONS ON PRODUCT USE

- Toshiba Corporation, and its subsidiaries and affiliates (collectively “TOSHIBA”), reserve the right to make changes to the information in this document, and related hardware, software and systems (collectively “Product”) without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA’s written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product’s quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before creating and producing designs and using, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the “TOSHIBA Semiconductor Reliability Handbook” and (b) the instructions for the application that Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications. **TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS’ PRODUCT DESIGN OR APPLICATIONS.**
- Product is intended for use in general electronics applications (e.g., computers, personal equipment, office equipment, measuring equipment, industrial robots and home electronics appliances) or for specific applications as expressly stated in this document. Product is neither intended nor warranted for use in equipment or systems that require extraordinarily high levels of quality and/or reliability and/or a malfunction or failure of which may cause loss of human life, bodily injury, serious property damage or serious public impact (“Unintended Use”). Unintended Use includes, without limitation, equipment used in nuclear facilities, equipment used in the aerospace industry, medical equipment, equipment used for automobiles, trains, ships and other transportation, traffic signaling equipment, equipment used to control combustions or explosions, safety devices, elevators and escalators, devices related to electric power, and equipment used in finance-related fields. Do not use Product for Unintended Use unless specifically permitted in this document.
- Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- **ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE FOR PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (2) DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO SALE, USE OF PRODUCT, OR INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.**
- GaAs (Gallium Arsenide) is used in Product. GaAs is harmful to humans if consumed or absorbed, whether in the form of dust or vapor. Handle with care and do not break, cut, crush, grind, dissolve chemically or otherwise expose GaAs in Product.
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). Product and related software and technology may be controlled under the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export Administration Regulations. Export and re-export of Product or related software or technology are strictly prohibited except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. Please use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. TOSHIBA assumes no liability for damages or losses occurring as a result of noncompliance with applicable laws and regulations.