

# M54640P

# **Stepper Motor Driver**

REJ03F0042-0100Z Rev.1.0 Sep.19.2003

### **Description**

The M54640P is a semiconductor IC to drive a stepper motor by the bipolar method.

### **Features**

- Bipolar and constant-current drive
- Wide current control rage (20 800mA)
- Wide supply voltage drive range (10 40V)
- Built in flywheel diodes
- Current level can be changed by steps or continuously.
- Built in a thermal shutdown circuit

### **Application**

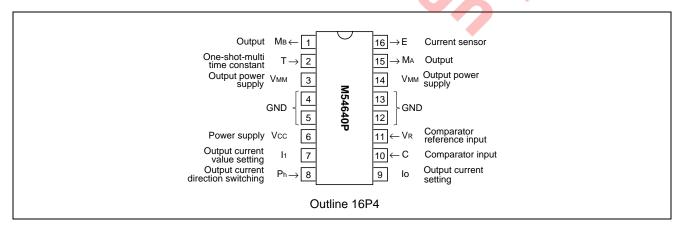
Printer, FDD, HDD, Fax

### **Function**

The M54640P drives a stepper motor by the bipolar drive method to change the current direction of a single coil and controls the current direction with PHASE input pin. In order to obtain higher efficiency, the constant current drive system to control the coil current is introduced. The current value can be selected among four levels (0 to max.) by selecting the combination of three internal comparators by logic input. It also can be continuously changed by controlling the reference voltage. Conversion to voltage is conducted by the current value sensing resistor (Rs) and the voltage is sensed with each comparator, and then each comparator output triggers monomulti and the current is cut for a certain time (tope) by utilizing the inductance of the coil.

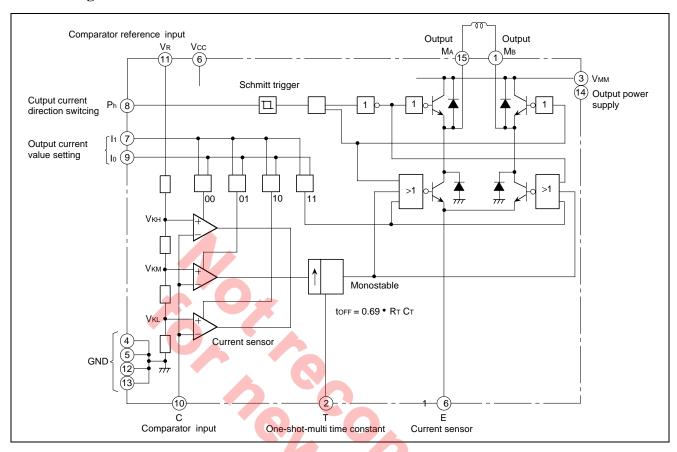
Also, diodes needed for choppering and a thermal shutdown circuit as a countermeasure against overvoltage are built in this circuit.

### **Pin Configuration**





### **Block Diagram**



# **Absolute Maximum Ratings**

( $Ta = 25^{\circ}C$ , unless otherwise noted.)

Parameter	Symbol	Ratings	Unit Conditions
Supply voltage	V <sub>cc</sub>	-0.3 to 7	V
Output supply voltage	$V_{_{\mathrm{MM}}}$	-0.3 to 45	V
Logic input voltage	$V_{\scriptscriptstyle L}$	-0.3 to 6	V
Analog input voltage	$V_c$	-0.3 to Vcc	V
Comparative input voltage	$V_{_{R}}$	-0.3 to 15	V
Logic input current	I <sub>L</sub>	-10	mA
Analog input current	I <sub>c</sub>	-10	mA
Output supply current	I <sub>MM</sub>	±1000	mA
Power dissipation	Pd	1.92	W Mounted on a board
Operating temperature	Topr	-20 to 75	°C
Storage temperature	Tstg	-55 to 125	°C

Note: Every voltage value is measured when the voltage at GND pin is 0V. The maximum and the minimum of each voltage value are shown in absolute values.

Regarding current directions, inflow current is shown in a positive value and outflow current is shown in a negative value. The maximum and the minimum of each current value are shown in absolute values.

# **Recommended Operating Condition**

( $Ta = 25^{\circ}C$ , unless otherwise noted.)

		Limits	Тур.	Max.	 Unit	
Parameter	Symbol	Min.				
Supply voltage	V <sub>cc</sub>	4.75	5	5.25	V	
Output supply voltage	V <sub>MM</sub>	10		40	V	
Output current	I <sub>o</sub>	20		800	mA	
Logic input rise time	t <sub>PLH</sub>			2	μS	
Logic input fall time	t <sub>PHL</sub>			2	μS	
Thermal shutdown temperature	T <sub>on</sub>		175	_	°C	

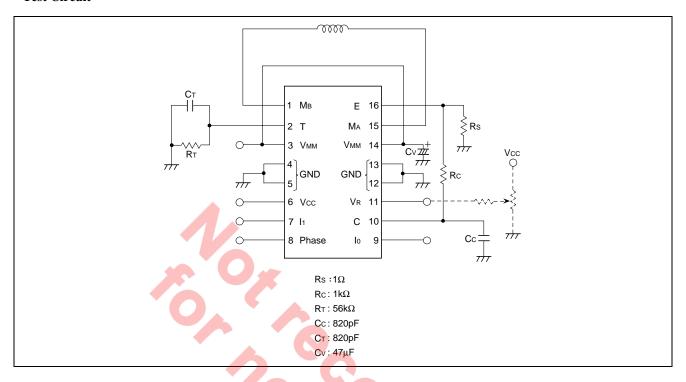
### **Electrical characteristics**

(Ta = 25°C,  $V_{cc}$  = 5.0V, unless otherwise noted.)

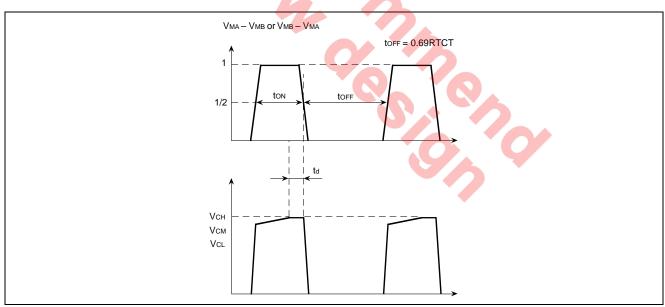
			Limit	S			
Parameter		Symbol	Min.	Тур.	Max.	Unit	Test conditions
Logic input voltage	"H"	V <sub>IH</sub>	2.0		V <sub>cc</sub>	V	V <sub>cc</sub> =5V
	"L"	V <sub>IL</sub>	0		0.8	_	
Comparator threshold	t	V <sub>ch</sub>	400	430	450	mV	V <sub>R</sub> =5V, I0=I1=0
		V <sub>CM</sub>	240	260	280	_	V <sub>R</sub> =5V, I0=1,I1=0
		V <sub>CL</sub>	75	90	100	_	V <sub>R</sub> =5V, I0=0, I1=1
Comparator input cur	rent	I <sub>co</sub>	-20		20	μΑ	
Output cutoff current		I <sub>OFF</sub>			100	μΑ	I0=I1=1(Ta=25°C)
Saturation voltage		Vsat			4.0	V	The voltage at the sensing resistor is
							not included. I <sub>o</sub> =500mA
Cutoff time		$t_{_{\mathrm{OFF}}}$	25	30	35	μS	$V_{MM}=10V, t_{ON} \ge 5\mu s$
Turnoff delay		td	`	1.6	2.0	μS	Ta=25°C, dVK/dt ≥ 50mV/μs
Supply current		I <sub>cc</sub>			25	mA	V <sub>cc</sub> =5V
Logic input current	"H"	I <sub>IH</sub>			20	μА	V <sub>i</sub> =2.4V
	"L"	I <sub>IL</sub>			-0.4	mA	V <sub>i</sub> =0.4V

# **Switching Characteristics**

### **Test Circuit**



# **Switching Waveforms**



### **Application Description**

### PHASE INPUT

Phase input decides the output mode.

Phase	MA	MB
Н	Н	L
L	L	Н

### • I0, I1

I0 and I1 fixed based on the comparison voltage  $V_{\scriptscriptstyle R}$  decide the output current level.

The current level can be continuously changed by changing the voltage at  $V_{\scriptscriptstyle R}$  continuously.

10	I1	Current level
Н	Н	0
L	Н	Low
Н	L	Average
L		High

### Current sensor

When the voltage fall at the current sensing resistor and the selected current level becomes of the same level, the comparator triggers the monostable. Then, the output stage is cut off for a certain time ( $t_{OFF}$ ). During this cutoff time, the current volume decreases slightly and falls short of the comparison level.

After the cutoff time  $(t_{OFF})$ , the output stage is in ON state again.

This operation is repeated.

### • Single pulse generator

At the comparator output rise edge, the monostable is triggered.

The pulse width of the monostable at the external timing Rt and Ct is as follows.

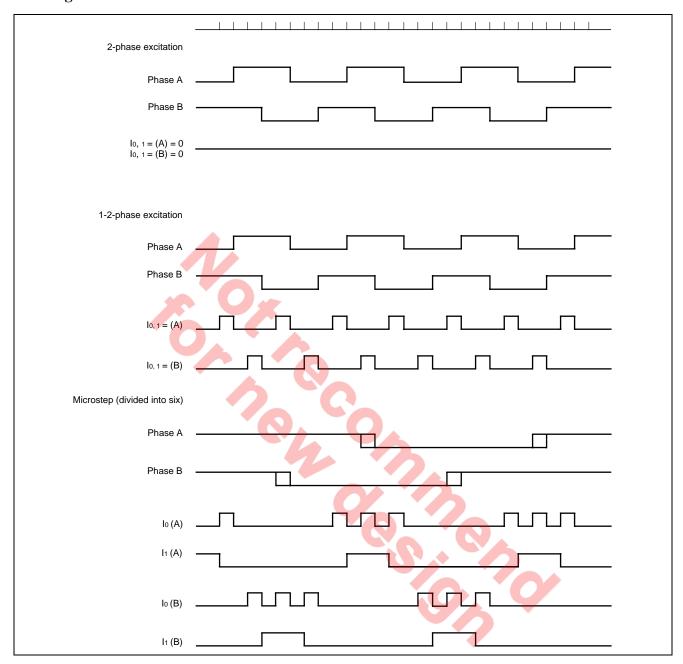
$$t_{OFF} = 0.69 \text{ x RtCt}$$

Retrigger during t<sub>OFF</sub> is neglected.

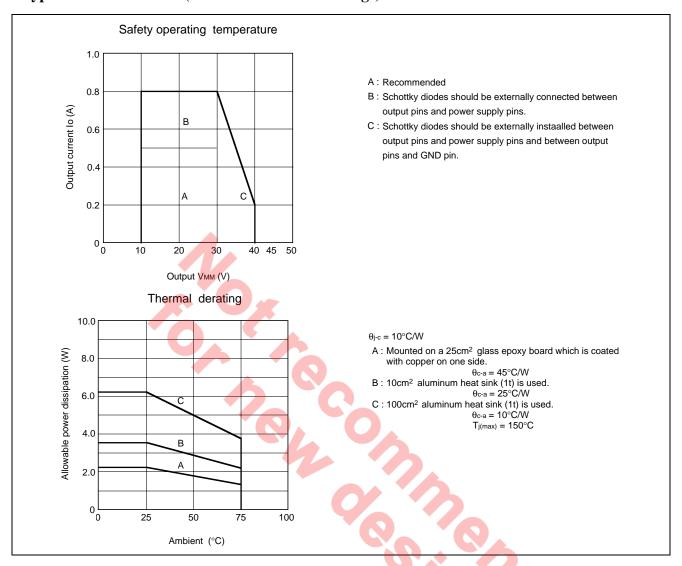
#### • Analog control

The output current level can be continuously changed by changing the voltage at  $V_R$  or the feedback voltage to the comparator.

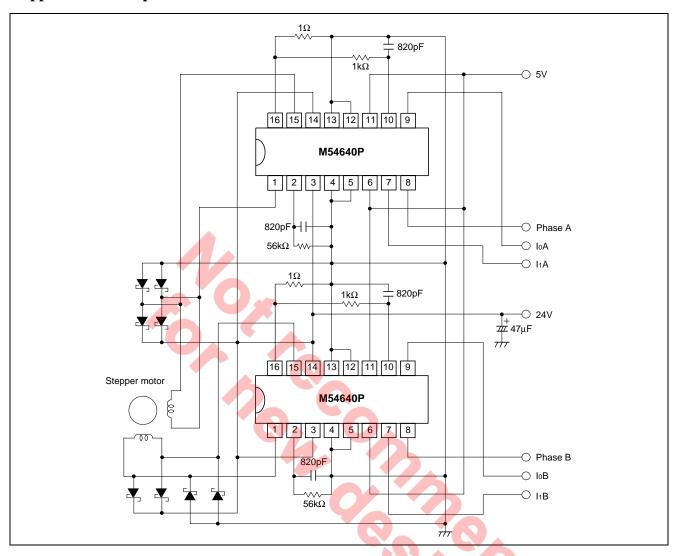
# **Timing Chart**



# **Typical Characteristics (Absolute maximum ratings)**



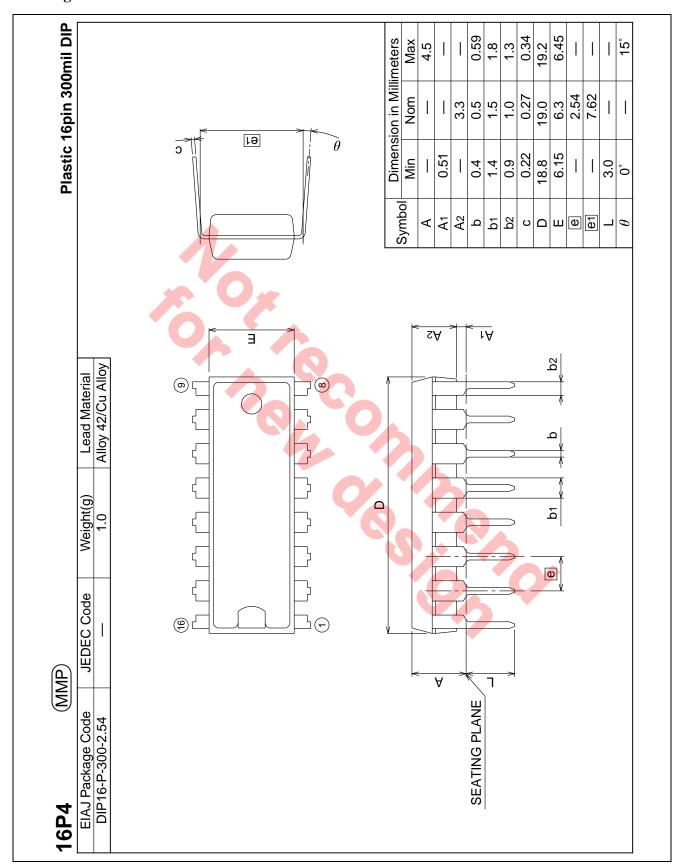
### **Application Example**



### **Precautions for use**

- (1) When the whole output current changes by a large margin (for example, when overheat protection operation causes intermittent flow of output current), the supply voltage may undergo a change. Therefore, selection and wiring of power supply should be conducted cautiously to avoid such a situation that the supply voltage exceeds the absolute maximum ratings.
- (2) When the supply voltage changes by a large margin, the operation of this IC may become unstable. In this case, the change of supply voltage can be controlled by connecting a capacitor at the point near to IC pin between Vcc pin and GND pin. (See above application example.)
- (3) Thermal shutdown function
- The state of thermal shutdown operation may differ according to the way of wiring within a board. Therefore, sufficient board evaluation should be conducted before use. When the board is changed, operation on the replacing board should be evaluated.
- The circuit board on which this IC is mounted is designed to realize low impedance between power supply and output pin.
  - Therefore, it is desirable to take a safe measure such as fixing a fuse to avoid such a situation that the board is damaged by a fire when output pin is internally short-circuited by excessively applied surge voltage by accident.

# **Package Dimensions**



Renesas Technology Corp., Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

Keep safety first in your circuit designs!

Reep sately lins; in your circuit designs, and it is a sately line. The sately lines in your circuit designs, and it is a sately line in the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.

Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corp. product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corp. or a third party.

2. Renesas Technology Corp. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.

3. All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corp. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor for the latest product information described here may contain technical inaccuracies or typographical errors.

Renesas Technology Corp. assumes no responsibility for any damage, liability, or other loss resident product distributor for the latest product home page (http://www.renesas.com).

4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Renesas Technology Corp. assumes no responsibility for any damage, liability or other loss resulting from the information on products. Renesas Technology Corp. assumes no responsibility for any damage, liability or other loss resulting from the information on products. Renesas Technology Corp. assumes no responsibility for any damage, liability or other loss resulting from the information contained

- use.

  6. The prior written approval of Renesas Technology Corp. is necessary to reprint or reproduce in whole or in part these materials.

  7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.

  Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.

  8. Please contact Renesas Technology Corp. for further details on these materials or the products contained therein.



**RENESAS SALES OFFICES** 

http://www.renesas.com

**Renesas Technology America, Inc.** 450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500 Fax: <1> (408) 382-7501

Renesas Technology Europe Limited.

Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, United Kingdom Tel: <44> (1628) 585 100, Fax: <44> (1628) 585 900

Renesas Technology Europe GmbH Dornacher Str. 3, D-85622 Feldkirchen, Germany Tel: <49> (89) 380 70 0, Fax: <49> (89) 929 30 11

Renesas Technology Hong Kong Ltd. 7/F., North Tower, World Finance Centre, Harbour City, Canton Road, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2375-6836

Renesas Technology Taiwan Co., Ltd. FL 10, #99, Fu-Hsing N. Rd., Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd. 26/F., Ruijin Building, No.205 Maoming Road (S), Shanghai 200020, China Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.

1, Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65.6213\_000\_Fay: <65.5678\_8001

16. NOV 0210-0200, 1 ax. NOV 0210-0001								
	0 0200, 1 02.1	0 0200, 1 d.x. 305 0210 0001	0 0200, 1 02. 30.0 32.10 3001	0.0200, 1.02	0.0250, 1 0.0. 0270 0001			