

MOS FIELD EFFECT TRANSISTOR

2SK1958

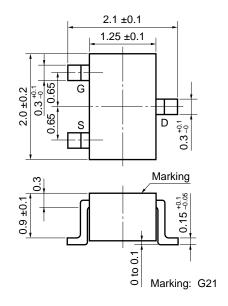
N-CHANNEL MOS FET FOR HIGH-SPEED SWITCHING

The 2SK1958 is an N-channel vertical MOS FET. Because it can be driven by a voltage as low as 1.5 V and it is not necessary to consider a drive current, this FET is ideal as an actuator for low-current portable systems such as headphone stereos and video cameras.

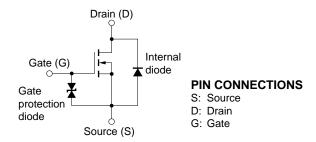
FEATURES

- · Gate can be driven by 1.5 V
- Because of its high input impedance, there's no need to consider drive current
- Since bias resistance can be omitted, the number of components required can be reduced

PACKAGE DIMENSIONS (in mm)



EQUIVALENT CURCUIT



ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

PARAMETER	SYMBOL	TEST CONDITIONS	RATING	UNIT
Drain to Source Voltage	VDSS	Ves = 0	16	V
Gate to Source Voltage	Vgss	V _{DS} = 0	±7.0	V
Drain Current (DC)	I _{D(DC)}		±0.1	А
Drain Current (Pulse)	ID(pulse)	PW ≤ 10 ms, duty cycle ≤ 50 %	±0.2	Α
Total Power Dissipation	Рт		150	mW
Channel Temperature	Tch		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C



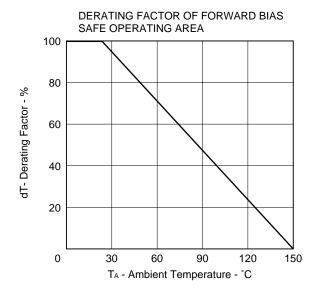
ELECTRICAL CHARACTERISTICS (TA = 25 °C)

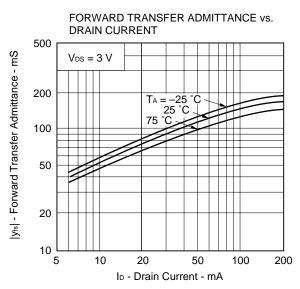
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Drain Cut-Off Current	Ipss	V _{DS} = 16 V, V _{GS} = 0			1.0	μΑ
Gate Leakage Current	Igss	$V_{GS} = \pm 7.0 \text{ V}, V_{DS} = 0$			±3.0	μΑ
Gate Cut-Off Voltage	V _{GS(off)}	$V_{DS} = 3 \text{ V}, I_{D} = 10 \mu \text{A}$	0.5	0.8	1.1	V
Forward Transfer Admittance	y _{fs}	V _{DS} = 3 V, I _D = 10 mA	20			mS
Drain to Source On-State Resistance	RDS(on)1	Vgs = 1.5 V, ID = 1 mA		20	50	Ω
Drain to Source On-State Resistance	RDS(on)2	Vgs = 2.5 V, ID = 10 mA		7	15	Ω
Drain to Source On-State Resistance	RDS(on)3	Vgs = 4.0 V, ID = 10 mA		5	12	Ω
Input Capacitance	Ciss	V _{DS} = 3 V, V _{GS} = 0, f = 1.0 MHz		10		pF
Output Capacitance	Coss			13		pF
Reverse Transfer Capacitance	Crss			3		pF
Turn-ON Delay Time	td(on)	$V_{\text{DD}} = 3 \text{ V, ID} = 10 \text{ mA, V}_{\text{GS(on)}} = 3 \text{ V,}$ $R_{\text{G}} = 10 \Omega, \text{ RL} = 300 \Omega$		15		ns
Rise Time	tr			70		ns
Turn-OFF Delay Time	td(off)			100		ns
Fall Time	t f			110		ns

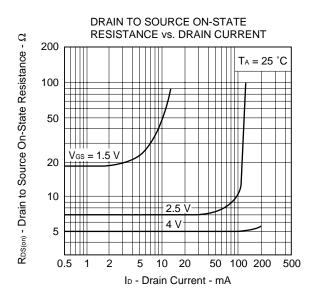
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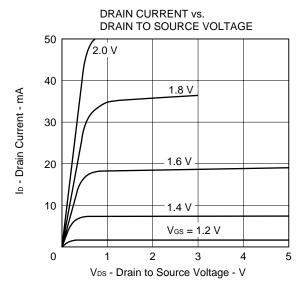


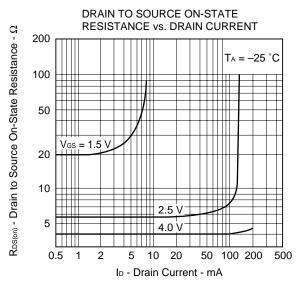
TYPICAL CHARACTERISTICS (TA = 25 °C)

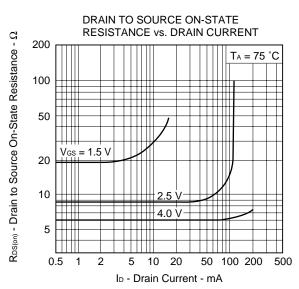




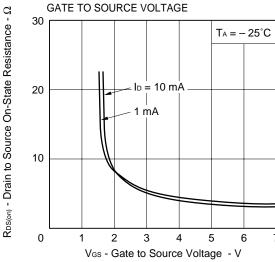


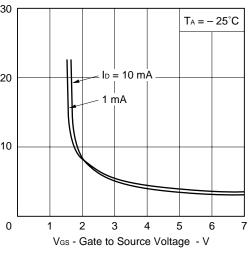




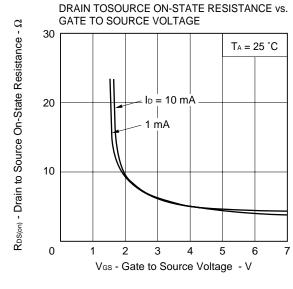


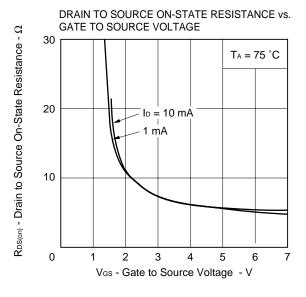


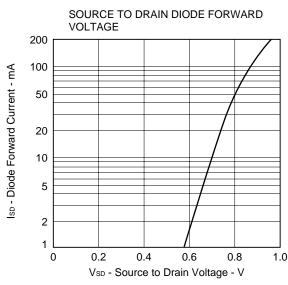


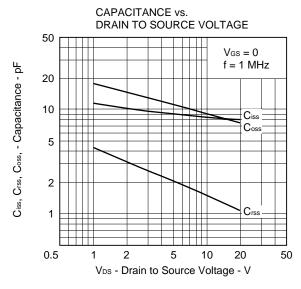


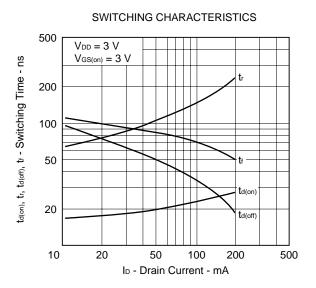
DRAIN TO SOURCE ON-STATE RESISTANCE vs.













REFERENCE

Document Name	Document No.		
NEC semiconductor device reliability/quality control system	TEI-1202		
Quality grade on NEC semiconductor devices	IEI-1209		
Semiconductor device mounting technology manual	C10535E		
Guide to quality assurance for semiconductor devices	MEI-1202		
Semiconductor selection guide	X10679E		

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