

SANYO Semiconductors DATA SHEET

LA6358NMJM — High-Performance Dual Operational Amplifier

Overview

The LA6358NMJM is a high-performance dual operational amplifier that can operate from a single voltage power supply. It features a built-in phase correction circuit. It can also operate from a dual power supply with both positive and negative levels and features low power consumption. The LA6358NMJM can be used in a wide range of industrial applications as a transducer amplifier for all types of transducers, as a DC amplifier circuit, and for other purposes as well.

Functions

• High-performance dual operational amplifier

Specifications

Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max		32	V
Differential input voltage	V _{ID}		32	V
Maximum input voltage	V _{IN} max		-0.3 to +32	V
Allowable power dissipation	Pd max	Ta≤25°C	300	mW
Operating temperature	Topr		-40 to +85	°C
Storage temperature	Tstg		-55 to +150	°C

Recommended Operating Conditions at Ta = -40 to $+85^{\circ}C$

Parameter	Symbol	Conditions	Ratings			1.1 14
			min	typ	max	Unit
Supply voltage	Vcc		3		24	V

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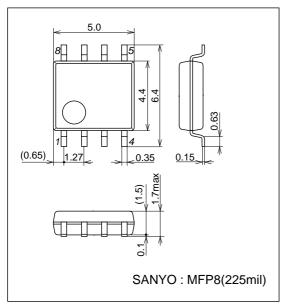
SANYO Semiconductor Co., Ltd. TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

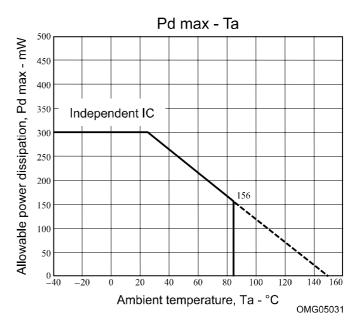
Electrical Characteristics at $Ta = 25^{\circ}C$, $V_{CC} = 5V$

Parameter	Symbol	Conditions	Test	Ratings			
			circuit	min	typ	max	Unit
Input offset voltage	VIO		1		±2	±3	mV
Input offset current	IIO	I _{IN} (+)/I _{IN} (-)	2		±5	±50	nA
Input bias current	Ι _Β	I _{IN} (+)/I _{IN} (-)	3,4		45	250	nA
Common-mode input voltage range	VICM		5	0		V _{CC} -1.5	V
Common-mode rejection ratio	CMR	V _{CC} = 30V	5	65	80		dB
Large-amplitude voltage gain	VG	$V_{CC} = 15V, R_L \ge 2k\Omega$	6	25	100		V/mV
Output voltage range	VOUT			0		V _{CC} -1.5	V
Supply voltage rejection ratio	SVR		11	65	100		dB
Channel separation	CS	f = 1k to 20kHz	7		120		dB
Current drain	Icc		8		0.5	1.2	mA
Output current (source)	I _{O source}	$V_{IN}^{+} = 1V, V_{IN}^{-} = 0V$	9	20	40		mA
Output current (sink)	IO sink	$V_{IN}^{+} = 0V, V_{IN}^{-} = 1V$	10	10	20		mA

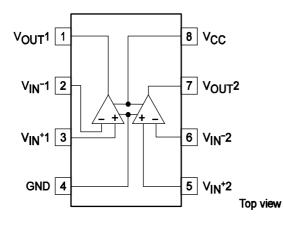
Package Dimensions

unit : mm 3032D



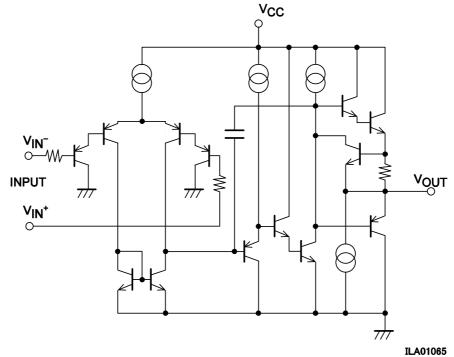


Pin Assignment



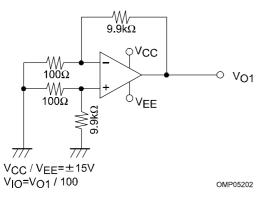


Equivalent Circuit

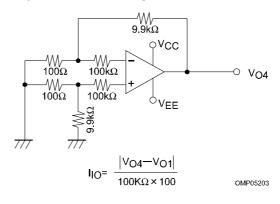


Test Circuits

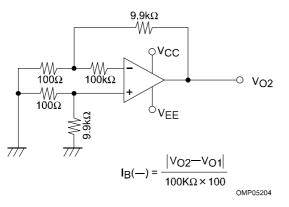
1. Input offset voltage VIO

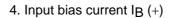


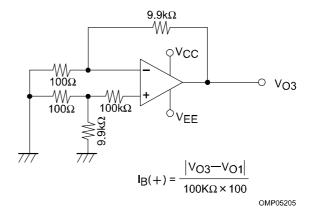
2. Input offset current IIO



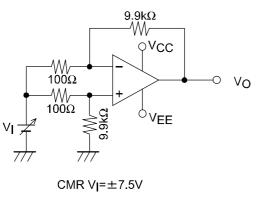
3. Input bias current IB (-)





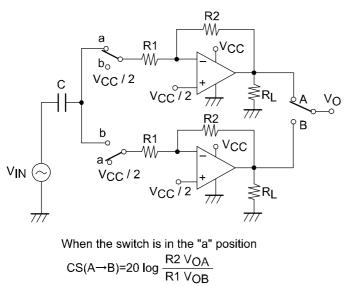


5. Common-mode rejection ratio CMR Common-mode input voltage range VICN

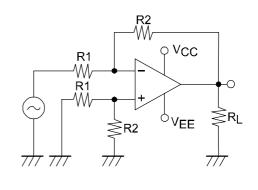


 $CMR=20log \frac{15 \times 100}{|\Delta V_O|}$

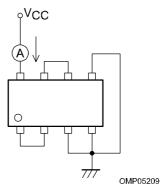
7. Channel separation CH sep

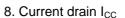


When the switch is in the "b" position $CS(B \rightarrow A)=20 \log \frac{R2 V_{OB}}{R1 V_{OA}}$ 6. Voltage gain VG

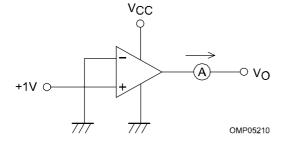


VG= <u>R2</u> R1 _{OMP05207}



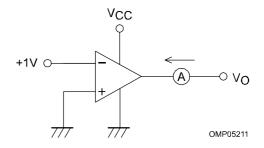


9. Output current Io source



10. Output current $I_{\text{O}}\xspace$ sink

OMP05208



LA6358NMJM

11. Supply voltage rejection ratio SVR (+)

9.9kΩ R2 R1 .Vcc -₩-100Ω οVο -₩/-100Ω VEE 9.9kΩ SVR -V_{CC}=15V,5V 7/7 VEE=-5V,-15V $\Delta V_{CC} \times 100$ SVR(+)=20log



4.0

3.5

3.0

2.5

2.0

1.5

1.0

0.5

0 0

60

55

50

45

40

35 30

25

20 15

10

5 0

24

20

16

12

0

1k

2 3

5

Output voltage amplitude, VoUT - Vp-p

-20-10 0

Output current IO (source) - mA

Current drain, I_{CC} - mA

Vccq A

7

10

ICC - VCC

20

Vo-f

Ç

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^{10k} ² ³ Frequency, f - Hz 2 3

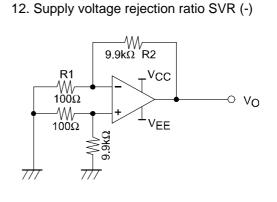
-**| |**^{1kΩ}-

Vin

7V

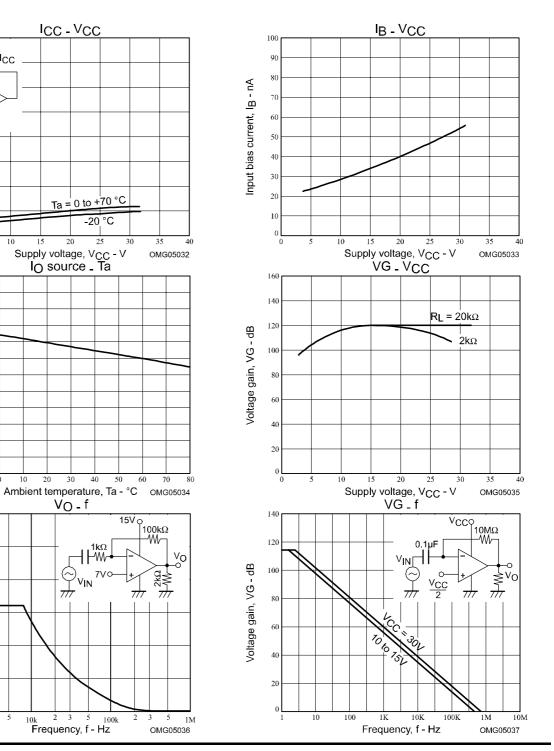
25

15







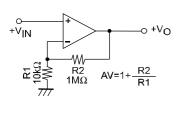


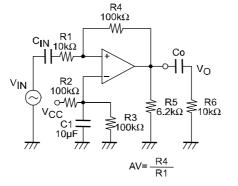
Application Circuit Examples

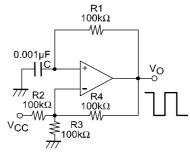
Noninverting DC amplifier

Inverting AC amplifier

Square wave generator







OMB05077

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